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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/752,042	12/29/2000	David I. Poisner	42390.P10586	9023

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[REDACTED] EXAMINER

VO, TIM T

[REDACTED] ART UNIT [REDACTED] PAPER NUMBER

2189

DATE MAILED: 08/04/2003

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Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary

Application No.

09/752,042

Applicant(s)

POISNER ET AL.

Examiner

Tim T. Vo

Art Unit

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 02 June 2003.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-12 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-12 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 29 December 2000 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) The proposed drawing correction filed on _____ is: a) approved b) disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) The translation of the foreign language provisional application has been received.
- 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413) Paper No(s). _____.
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) Notice of Informal Patent Application (PTO-152)
3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____. 6) Other: _____

Part III DETAILED ACTION

Notice to Applicant(s)

This application has been examined. Claims 1-12 are pending.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

1. Claims 1-12 are rejected under 35 U.S.C. § 102(b) as being anticipated by

Gephardt et al. European patent application 0676686 A2 referred hereinafter

"Gephardt".

2. As for claims 1, 5 and 9, Gephardt teaches a method comprising:

asserting an edge-triggered interrupt signal from an input/output interrupt

controller to a local interrupt controller within a processor (see figure 2, peripheral

device 204 from external bus 206 generate interrupts to the PM MESSAGE UNIT 232,

wherein the PM MESSAGE comprises interrupt detector 306 also the interrupt detector

306 coupled to the interrupt controller 224 and page 4 lines 51-58, wherein peripheral

devices 204 send request on bus 206 to the integrated processor 202 via bus interface

228); and

delivering an interrupt pending signal from the processor to a power management device (see figure 2, integrated processor 202, power management unit 208 and PM message bus 210 and page 5 lines 6-41, wherein the integrated processor receives request detects from the bus request detector 302. The integrated processor then generates PM messages to the power management unit 260, wherein the PM messages comprises all of the messages in table I which included a timer interrupt is pending or in service).

3. As for claims 2, 6 and 10, Gephardt teaches the power management device causing the processor to enter a high power state if the processor is in a low power state when the processor delivers the interrupt pending signal to the power management device (see figure 5, state 502, state 504, and figure 6, state 602, 604, 606 revert back to ready state 600 and page 5 lines 48-57 and page 6 lines 46-54, wherein figure 5 discloses the integrated processor 202 is in idle mode 502 and activity detected 504 when there is an interrupt, thus figure 6 discloses the integrated processor 202 is dynamically switching from ready state 600 (high power state) to suspend state 606 (low power state) and vice versa from (suspend state (low power state) ready state 600 (high power state)).

4. As for claims 3, 7, and 11, Gephardt teaches wherein delivering an interrupt pending signal includes delivering the interrupt signal from the processor to the power management device over a signal line coupled between a single processor pin and the power management device (see figure 2, integrated processor 202, PM message 210, power management state machine 260 and page 5 lines 6-41, wherein the integrated

processor receives request detects from the bus request detector 302. The integrated processor then generates PM messages to the power management unit 260, wherein the PM messages comprises all of the messages in table I which included a timer interrupt is pending or in service).

5. As for claims 4, 8, 12, Gephardt teaches wherein causing the processor to enter a high power state includes the power management device deasserting a stop clock signal (see figure 7, power management state machine 260, clock control unit 264, stop CPU signal line, 202, wherein the power management sent a control stop signal to the integrated processor 202).

Response to Amendment

Applicant amended claims 1 and 5, wherein asserting an edge-triggered interrupt signal from an input/output interrupt controller to a local interrupt controller within a processor. Gephardt anticipates these limitations i.e. figures 2-3 and page 4 line 51 to page 5 line 6, discloses a PM message unit 232 comprises an interrupt detector 306 wherein the interrupt detector 306 and PM message unit 232 detect various interrupts such as bus requests, interrupts from external bus master 204, SMI, NMI interrupts and page 4 lines 58 discloses interrupt detector 306 coupled to the interrupt controller 224.

Conclusion

6. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not

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mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tim T. Vo whose telephone number is 703-308-5862. The examiner can normally be reached on 7:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark Rinehart can be reached on 703-305-4815. The fax phone numbers for the organization where this application or proceeding is assigned are 703-746-7239 for regular communications and 703-746-7238 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-2100.



Tim T. Vo
Examiner
Art Unit 2189

T.V
July 16, 2003